Seat No.: 2965

NL-116

November-2017

B.Sc., Sem.-V

CC-302: Electronics

[Max. Marks: 70 Time: 3 Hours] All the questions carry equal marks. Instructions: (1) Symbols have their own meaning. (2)'Draw a circuit of mod 5 & 2 × mod 5, mod 10 counter and explain its working in 10 detail. OR Draw a circuit of mod 3 and 2 × mod 3, mod 6 counter and explain its working in detail. Draw a circuit of synchronous, 4 bit up-down counter. OR Draw a circuit of asynchronous, 3 bit down counter. Draw the signal diagram of 8085 microprocessor and explain all the blocks in 10 2. (a) detail. OR Draw the timing diagram for execution of instruction MVI A, 36 H & explain all the T States in detail. Draw the timing diagram of memory read cycle and explain all T states. OR Draw the timing diagram of memory, write cycle and explain all T states. Write a program to perform the following function and verify the output: 10 Load 8 BH in regi D (1) Load 6 FH in regi C (2) Increment the content of regi. C by one (3) Add the content of regi C & D & display the sum in regi E. (4) OR P.T.O. NL-116

		Write a program to perform the following task:	10
		Load 91H in regi B & 87H in regi. C. Mask all the bits except Do from regi. B & C. If Do is at logic 1 in both the register, turn on the light connected to the Do of O/P port 01H, otherwise turn off the light.	
	(b)		4
		OR	
		In a memory mapped I/O, how does the microprocessor differentiate between an I/O & memory? Can an I/O have the same address as a memory register?	4
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4.		ite a program to perform the following task:	14
		et of 3 readings is stored in memory location starting at CO90 H. Sort the readings in ending order.	
	Dat	a (H) 89, 45, 32.	
	*	OR	
	Wri	ite a program to perform the following task:	14
	data	et of current readings is stored in memory location starting at DO60 H. The end of string is indicated by data byte OOH. Add the set of readings. The answer may be er than FFH. Display the entire sum at memory location EO70 H & EO71 H.	
	Data	a (H) 32, 62, F2, A1, 09, 00	
5.	(1)	Define Mealy Model.	14
	(2)	Define Moore Model.	
	(3)	What is ROM?	
	(4)	What is glitch?	
	(5)	What is the operating frequency of 8085 microprocessor?	
	(6)	Give the full form of ALE.	
	(7)	What is the difference between tri state buffer and latch?	
	(8)	OUT is a 2 byte instruction.	
	(9)	Explain about the following instructions:	
		(a) HLT .	
		(b) NOP	
		(c) XRAB	
		(d) ADI, 56 H	
		(e) STA CO50 H	
		(f) INX B	