Seat No.: 3313	
AB-118 April-2016	
B.Sc., SemVI	
CC-307: Electronics	
Time: 3 Hours] [Max. Marks: 70	
Instructions: (1) All questions carry equal marks.	
(2) Figures on the right indicate marks.	
(3) Clearly mention the options.	
1. (a) Draw the circuit of log amplifier using two Op-Amps and explain its operation.  Also obtain the expression for Vo	7
OR	
Discuss the differentiator circuit using Op-Amp.	
(b) Discuss the circuit of non-inverting comparator and explain its operation. Also draw its input, output waveforms for V <sub>ref</sub> Positive and V <sub>ref</sub> negative.	*
Draw the circuit of regenerative comparator (Schmitt trigger) and explain its	
operation.	
2. (a) Draw the block diagram of PLL and explain each block in brief.  OR	7
Draw the circuit of digital phase detector and explain its operation.	
(b) Discuss how PLL is used as a frequency multiplication and AM detection.  OR	7
Obtain the expression for lock in range related to PLL	
3. (a) Explain the construction and working of an SCR.  OR	7
Discuss full wave rectifier using SCR and derive the expression for average voltage and average current.	e
(b) Discuss the application of SCR as a power control.  OR	7
Draw V-I characteristics of an SCR and explain it.	
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Describe SCR equivalent circuit of Triac. OR Draw V-I characteristics of Triac and explain its operation. Discuss how Diac is used as a lamp dimmer. (b) OR How UJT is used as a relaxation oscillator? Also obtain the expression for frequency of saw-tooth wave. 5. Answer in one or two sentences: What are the limitations of ordinary Op-Amp differentiator? Draw t reuit of lossy integrator. Give difference between digital and analog PLL. Define capture range. Draw the circuit of AM phase detector. Define voltage to frequency conversion factor Ky. Define holding current, Why are SCRs usually used in a.c. circuit? In an SCR circuit how the angle of conduction can be changed? Y10) State advantages of SCR as a switch. Define peak inverse voltage of SCR.

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Which

13) Write full form of VCO.

14) List the applications of Triac.

stor that does not have the gate terminal?