Seat No.: 2339

AC-118

April -2018

B.Sc., Sem.-VI

CC-308: Electronics

Time: 3 Hours

Max. Marks: 70

4

- Instructions: (1) All the questions carry equal marks.
 - (2) Symbols have their own meaning.
- Explain about R-2R type D/A converter. Also draw the circuit of 4 bit D A converter using input gates, flip-flop, level amplifier & resistive divider network. 10

 OR

 Give logic diagram of 3 bit simultaneous type A/D converter & explain it's

Give logic diagram of 3 bit simultaneous type A/D converter & explain it s working in detail.

Calculate V_A for 5 bit resistive Divider network having input 10110. Here, 0 = 0 V & 1 = 10 V.

OR

Calculate the output voltage for binary ladder circuit having digital input 1000. Here, 0 = 0 V & 1 = 10 V.

Write a program to count 0 to 9 with 2 sec delay between each count. After count 9 it restart to 0 & repeat the sequence continuously. Clock frequency = 0.5 MHz. 10 OR

Write a program to count continuous hexadecimal numbers FFH to OOH in a system of 0.5 µs. Use register C to set up 10 ms delay between each count.

- Calculate internal delay and total delay for the following program having 1 T State = 0.5 µs. (any one)
 - (1) MVI B, 38 H
 - L-2 MVI C, FF H
 - DCR C
 JNZ LOOP-1
 DCR B
 JNZ LOOP-2
 - (2) MVI B, 42 H
 - L-2 MVIC, DD, H
 - L-1 DCR C

 JNZ LOOP-1

 DCR B

 JNZ LOOP-2

3		(a)	Write a program to	provid	e the given O	N/OFF tir	ne to	3 traffic lights	(G, Y & R)	10
			& two pedestrian s	igns (wa	ark & Don i	ON tit				
			Green		Data bits	20 se				
			Yellow		D_3	5 sec	2			
			Red		D ₅	25 se	С			
			Walk		-	20 se	С			
			Don't walk		D ₆	30 se				
			Don't Walk		D_7	50 50				
			The traffic & pede the road when gree	estrian f n light i	low are in sa	me direct	ion a	nd pedestrian	should cross	
			Write a 20 ma 4		OR			mair BC Clear	the Z flag	
			Write a 20 ms tin without affecting as	ny other	subroutine flags in the f	lag registe	er & 1	return to the ma	in program.	
	(b)	Explain about 8 RS		actions of 808	5.				4
)R	. 1	ALT O DI	т р	LICH & DOD		
			Give difference &	Similari	ty between C	ALL&RI	ы, г	USH & FUI.		
4.	(a		Draw the block dia MODE O as simple			explain eac	ch blo	ck in detail. A	Also explain	10
					OR					
			Explain DAC 080 application.)8 givii	ng its featur	e, pin co	nfigu	ration, block	diagram &	
	(b))	Explain about contr	ol word	of 1C 8255	Α.	-			4
		7	Write a program to	generat	e square wav	e.				
				9.						
5.	An	swe	er in short :							14
	(1)	_			uch as DCX		not a	affect flags.		
	(2)	(Counter & time dela							
	(3)	-	is a group of memory location.							
	(4)	- V	A large software projected is usually divided into subtasks called IC 8255A is a pin DIP.							
	(5)	1		_ pin L	Mr.					
	(0)		Vhat is SAR?							
	101	13	Vhat is SAR?	v test ?						
1	(9)		Vhat is monotonicit xplain about the fo			:				
		G) CC	(ii)	CZ		(iii)	CP		
		(1	v) RM	(v)	RP		(vi)	RPE		
			,,	1, 1						